



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

Bib Data Sheet

CONFIRMATION NO. 4252

SERIAL NUMBER 10/715,929	FILING DATE 11/18/2003 RULE	CLASS 365	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 5201-27100
-----------------------------	---------------------------------------	--------------	------------------------	--------------------------------------

APPLICANTS

Subramanian Ramesh, Cupertino, CA;
 Ruggero Castagnetti, Menlo Park, CA;
 Ramnath Venkatraman, San Jose, CA;

** CONTINUING DATA ***** *NONE* *DL*

** FOREIGN APPLICATIONS ***** *NONE* *DL*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 02/17/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Blum</i> Examiner's Signature Initials	STATE OR COUNTRY CA	SHEETS DRAWING 8	TOTAL CLAIMS 36	INDEPENDENT CLAIMS 3
--	---	---------------------------	------------------------	-----------------------	----------------------------

ADDRESS
 Leo Peters
 LSI Logic Corporation
 MS D-106
 1621 Barber Lane
 Milpitas , CA
 95035

TITLE
 Memory cell architecture for reduced routing congestion

FILING FEE RECEIVED 1058	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
------------------------------------	---	--